IN THE UNITED STATES PATENT & TRADEMARK OFFICE

ATTORNEY DOCKET NUMBER: 10991268-3

IN RE APPLICATION OF: U.S. Patent No. 7,412,468

USPTO CONFIRMATION CODE: 7201

APPLICATION NO.: 10/686,331

FILED: Oct. 14, 2003

EXAMINER: Chat C Do

GROUP ART UNIT: 2193

FOR: GENERATION OF CRYPTOGRAPHICALLY STRONG RANDOM NUMBERS USING MISRS

37 CFR 1.322 & 37 CFR 1.323 REQUEST FOR CERTIFICATE OF CORRECTION

HONORABLE COMMISSIONER OF PATENTS & TRADEMARKS

Sir:

The following is a request for a certificate of correction in Serial Number 10/686,331, now Patent Number 7,412,468.

A certificate of correction under 35 USC 254 is respectfully requested in the above-identified patent.

☐ All errors were the fault of the USPTO, no fee required. In the event that a further fee is required, please charge the amount to Deposit Account No. 082025.

☐ All errors were the fault of the applicant and, accordingly, please charge **$100.00** to our Deposit Account No. 082025. In the event that a further fee is required, please charge the amount to the same Deposit Account.

☒ The errors were the fault of both the applicant and USPTO and, accordingly, please charge **$100.00** to our Deposit Account No. 082025. In the event that a further fee is required, please charge the amount to the same Deposit Account.

The exact locations where the errors appear in the patent and patent application are as follows:

In column 10, line 58, in Claim 1, delete "MISRs." and insert - - MISRs; wherein the inputs of at least one of the number of MISRs are coupled to at least one of a data address bus which transfers data addresses between a data address cache and a CPU core, an instruction data bus which transfers instructions between an instruction data cache and a CPU core, and an instruction address bus which transfers instruction addresses between an instruction address cache and a CPU core. - -, therefor.

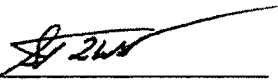(Amendment to Claims filed on July 03, 2008, page 2, in Claim 1, lines 7-12)

In column 12, line 27, in Claim 19, after "address" delete ":" and insert - - ; - -, therefor.

(Amendment to Claims filed on July 03, 2008, page 4, in Claim 22, line 4)

The requested corrections are attached on Form PTO 1050.

Respectfully Submitted

_9/25/2008_
DATE

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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

Page 1 of 1

PATENT NO. : 7,412,468

APPLICATION NO. : 10/686,331

ISSUE DATE : Aug. 12, 2008

INVENTOR(S) : Richard M. Butler

   It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

  In column 10, line 58, in Claim 1, delete "MISRs." and insert - - MISRs; wherein the inputs of at least one of the number of MISRs are coupled to at least one of a data address bus which transfers data addresses between a data address cache and a CPU core, an instruction data bus which transfers instructions between an instruction data cache and a CPU core, and an instruction address bus which transfers instruction addresses between an instruction address cache and a CPU core. - -, therefor.

  In column 12, line 27, in Claim 19, after "address" delete ":" and insert - - ; - - , therefor.

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